

**TRANSMITTAL**

Electronic Version v1.1

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**Title of  
Invention****OPTIMIZED CONDUCTIVE LID MOUNTING FOR INTEGRATED  
CIRCUIT CHIP CARRIERS**

Application Number :

Date :

First Named Applicant: Michael A. Gaynes

Confirmation Number:

Attorney Docket Number: FR920030002US1

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| Documents being submitted:   | Files   |
|--|---|
| us-assignment  | FR920030002US1-usassn.xml<br>us-assignment.xsl<br>us-assignment.dtd<br>FR920030002US1Assign1.tif<br>FR920030002US1Assign2.tif   |
| us-request   | FR920030002US1-usrequ.xml<br>us-request.dtd<br>us-request.xsl   |
| us-fee-sheet   | FR920030002US1-usfees.xml<br>us-fee-sheet.xsl<br>us-fee-sheet.dtd   |
| us-declaration   | FR920030002US1Dec2.tif  |
| us-declaration   | FR920030002US1Dec.tif   |
| application-body   | FR920030002US1spec-trans.xml<br>us-application-body.xsl<br>application-body.dtd<br>wipo.ent<br>mathml2.dtd<br>mathml2-qname-1.mod<br>isoamsa.ent<br>isoamsb.ent<br>isoamsc.ent<br>isoamsn.ent<br>isoamso.ent<br>isoamsr.ent<br>isogr3.ent<br>isomfrk.ent<br>isomopf.ent<br>isomscr.ent<br>isotech.ent<br>isobox.ent<br>isocyr1.ent<br>isocyr2.ent<br>isodia.ent<br>isolat1.ent<br>isolat2.ent<br>isonum.ent<br>isopub.ent<br>mmlextra.ent<br>mmlalias.ent<br>soextblx.dtd<br>fr920030002us1drawing1.tif<br>fr920030002us1drawing2.tif<br>fr920030002us1drawing3.tif<br>fr920030002us1drawing4.tif |
| <b>Comments</b>  |   |
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